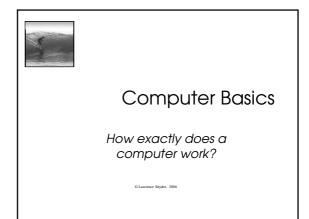
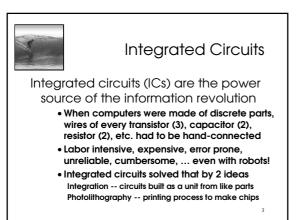


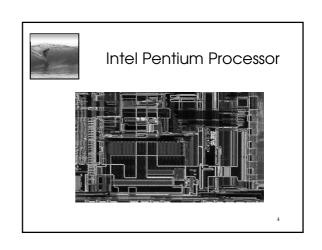
Announcements

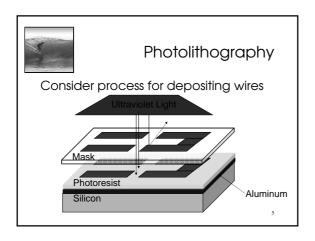
Project 2B turn-in Wednesday 11:00PM Midterm 2 on Friday

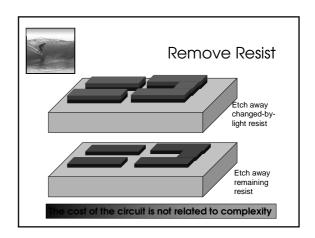
Only on material since last midterm

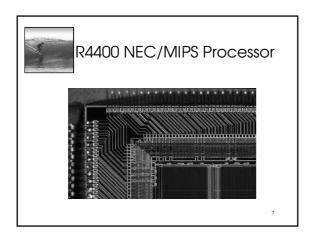


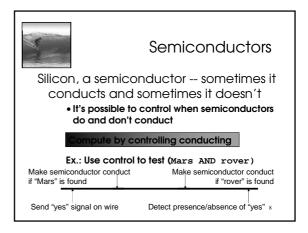


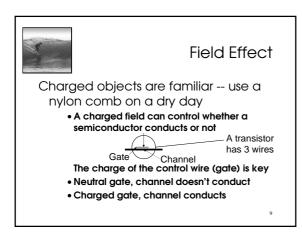


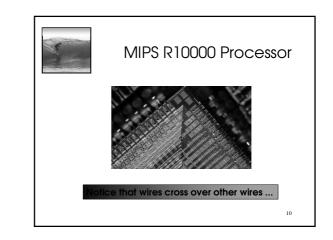


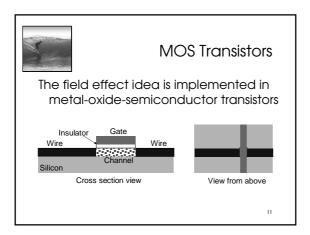


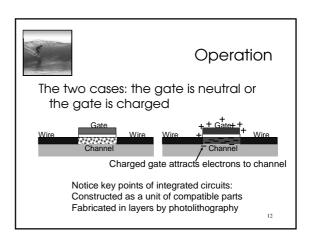


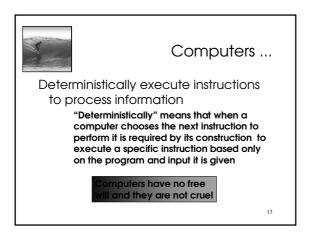


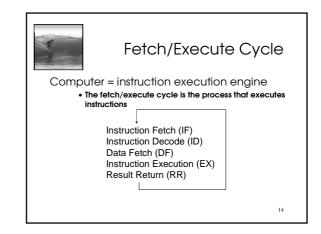


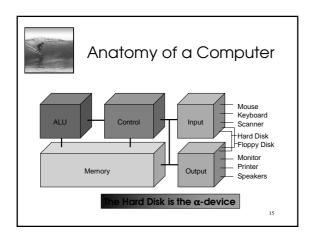


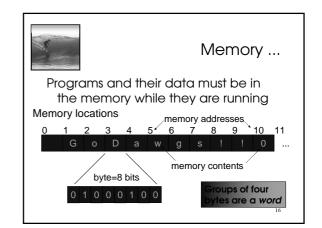


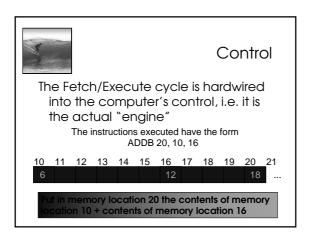


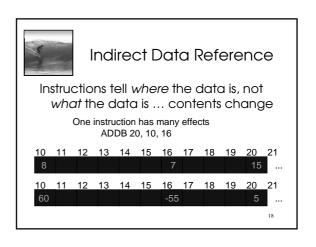




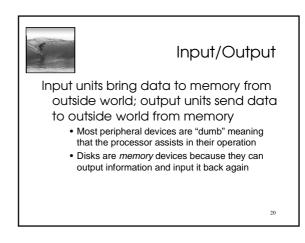


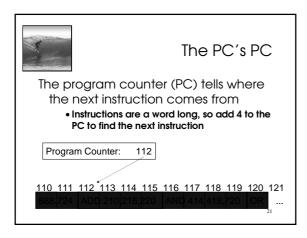






AL	U
The Arithmetic/Logic Unit does the	
actual computation	
Each type of data has its own separate instructions ADDB : add bytes ADDBU : add bytes unsigned ADDH : add half words ADDHU : add halves unsigned ADD : add words ADDU : add words unsigned ADDS : add short decimal numbers ADDD : add long decimal numbers	
Most computers have only about a 100-150 instructions hard wired	







Clocks Run The Engine

The rate a computer "spins around" the Fetch/Execute cycle is controlled by it's clock

- Current clocks run 2-3 GHz
- In principle, the computer should do one instruction per cycle, but often it fails to
- Modern processors try to do more than one instruction per cycle, and often succeed

rate is not a good indicator of speed

