Managing recompilation

- Mhat happens if a source file is changed?
 - Possibly need to recompile all the files that referenced it
- n How to do this?
 - n IDE: built-in
 - ь So far: by hand
 - Call javac on out-of-date source files, maybe re-jar
 - n But: tedious, error prone
 - n Tool-based approach: make a tool for it!

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make

- make is a great tool that manages any kind of process with dependencies
- n A Makefile describes rules for when something depends on something else, and what to do to make it up-to-date
 - based on file modification times stored with every Unix file
- Invoking make then runs these rules to decide what, if anything, needs to be done to bring things up-to-date

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Dependencies

- n Makefile includes lines of the form target...: source...
 - Means that each target depends on each source
 - If any of the sources are modified, then all the targets are considered out-of-date
- n Example:

main.class: main.java

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Actions

- For each dependency, can add an action to perform to bring the target(s) up-to-date
 - n Action is a series of shell command lines
 - n each line must start with a tab
 - n use /bin/sh syntax
- n Example:

main.class: main.java javac main.java

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Invoking make

- n make *target...*
 - uses Makefile in current directory to bring one or more targets up to date, using their actions
 - n does nothing if all targets up to date
 - if omit target arguments, then rebuild the first target in Makefile
 - n the default target
- _n Example:
 - > make main.class
 javac main.java
 >

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Controlling output

- $_{\rm n}$ By default, $_{\mbox{\scriptsize make}}$ prints out each action it performs
- Can disable printing an action by prefixing it with @
- _n Example:

```
main.class: main.java
@echo Compiling main.java...
@javac main.java
> make main.class
Compiling main.java...
>
```

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Dependency patterns

- Often have a simple rule over all files with certain naming patterns
 - □ Can use % in the target and source
 - Rule applies to any real targets and sources where % is replaced by the same thing on both sides
- n Example:

```
%.class: %.java
```

 $_{\rm m}$ Means that X. class depends on X. java

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Actions for patterns

- Actions for dependency patterns need to have patterns too
 - n \$@: the target
 - $_{\rm n}$ \$^: the source(s)
 - $_{\tt n}$ \$*: the thing matched by % in the rule
- _n Example:

```
%.class: %.java
    @echo -n "compiling class $* "
    @echo "($^ to $@)"
    javac $^
```

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Dependency trees

- n One target can depend on another target, ad nauseum
 - Dependency rules form a DAG (directed acyclic graph)
- make figures out how to rebuild a target by first making sure its sources are up-to-date, which may cause make to first rebuild them, recursively

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Example dependency tree

```
%.class: %.java
    javac $^
main.jar: main.class helper.class
    jar cfv $@ $^
install: main.jar
    cp $^ ${HOME}/bin
```

> make install javac main.java javac helper.java

jar cfv main.jar main.class helper.class
cp main.jar /homes/iws/myLogin/bin

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Makefile variables

- Can define variables in Makefiles, and use them in rules and actions
 - n VARNAME = REPLACEMENT..
 - n Referenced using \$ { VARNAME}
- _n Example:

```
JAVAC_FLAGS = -g
%.class: %.java
    @echo "compiling class $*"
    javac ${JAVAC_FLAGS} $^
```

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Substitutions in make vars

- _n Can do replacements in variables
 - n \${ VAR: oldPat=newPat}
 - □ oldPat and newPat can contain %
 - match each word in \$ { VAR} against oldPat, where % can match anything
 - n replace matches with new
 - if *new* contains %, substitute with what % matched
- n Good for adjusting extensions, prefixes

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Examples of substitutions

Make quiz

- n Extend Makefile SO that "make clean"
 removes all .class files
- Add a rule so that I can say "make foo. java.ps", for any foo. java, to format my Java source file using enscript -2r into a nicely formatted .ps file
- $_{\rm n}$ Add a rule to put all my $.{\tt class}$ files into a single . jar file
- Add a variable defining all the . java files in my application, and only clean, format, and archive those files

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