

YOUR NAME: _____

CSE401 Final (aka Second Midterm), December 10, 2008

- Open book, open note
- Closed electronics, closed neighbor
- 60 minutes (but I'll stay for 90 if anybody remains)
- Put your name clearly on top
- Legibility is a plus – if I can't read your answer, I won't try to read your mind

- *Don't open until you are told to*

3. (40 points total) A *stack machine* describes a computer architecture where instructions take no operands: most instructions, including load and store, implicitly operate on values at the top of the stack and replace those values with the result. (These arise both in actual processors, such as the Burroughs large system architecture, as well as in virtual processors, including the UCSD p-machine and the JVM instruction set.)
- a. (15 points total) From a compiler's point of view, concisely but clearly describe two distinct issues that would affect the Intermediate Code Generation phase due to the use of a stack machine as a target (instead of a register-based machine such as the x86).
- b. (15 points total) From a compiler's point of view, concisely but clearly describe two distinct issues that would affect the Target Code Generation phase due to the use of a stack machine as a target (instead of a register-based machine such as the x86).
- c. (5 points total) Describe one conventional optimization that would still be effective for a stack machine.
- d. (5 points total) Describe one conventional optimization that would no longer be effective for a stack machine.

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4. (10 points) From a technical point of view, describe (in 1-2 sentences each) two things you learned about arrays from the MiniJava assignment.

5. (10 points) Concisely describe two kinds of information that can be computed during one execution of a program and then used to better optimize the program for further executions.

6. (10 points) The MiniJava compiler, like all compilers, selects an evaluation order for code generation: for example, at a node representing a binary arithmetic operation, is the invocation to generate the code for the left operand made before the one for the right operand, or vice versa. Question: Might the order affect the total number of registers required to evaluate the subtree? (And, briefly, why?)

7. (10 points) Consider the following code snippet:

```
if (little-endian) then
    S1;
else
    S2;
endif
...
if (little-endian) then
    S3;
else
    S4;
endif
```

Assume **little-endian** is intended to be a boolean “configuration” variable that selects whether **little-endian** or **big-endian** representations will be used in the program. In what way does that affect, if at all, potential optimizations?