## CSE351

- Announcements:
- HWO, having fun?
- Use discussion boards!
- Sign up for cse351@cs mailing list
- If you enrolled recently, you might not be on it


## Today's topics

- Memory and its bits, bytes, and integers
- Representing information as bits
- Bit-level manipulations
- Boolean algebra
- Boolean algebra in C


## Hardware: Logical View



## Hardware: Semi-Logical View



Intel* P45 Express Chipset Block Diagram

## Hardware: Physical View



## Performance: It's Not Just CPU Speed

- Data and instructions reside in memory
- To execute an instruction, it must be fetched onto the CPU
- Then, the data the instruction operates on must be fetched onto the CPU
- $\mathrm{CPU} \Leftrightarrow$ Memory bandwidth can limit performance
- Improving performance 1: hardware improvements to increase memory bandwidth (e.g., DDR $\rightarrow$ DDR2 $\rightarrow$ DDR3)
- Improving performance 2: move less data into/out of the CPU
- Put some "memory" on the CPU chip
- The next slide is just an introduction. We'll see a more full explanation later in the course.


## CPU "Memory": Registers and Instruction



- There are a fixed number of registers on the CPU
- Registers hold data
- There is an I-cache on the CPU holding recently fetched instructions
- If you execute a loop that fits in the cache, the CPU goes to memory for those instructions only once, then executes out of its cache


## ■Introduction to Memory

## Binary Representations

- Base 2 number representation
- Represent $351_{10}$ as $0000000101011111_{2}$ or $101011111_{2}$

■ Electronic implementation

- Easy to store with bi-stable elements
- Reliably transmitted on noisy and inaccurate wires



## Encoding Byte Values

- Binary

$$
00000000_{2}--11111111_{2}
$$

- Byte $=8$ bits (binary digits)
- Decimal
- Hexadecimal

$$
\begin{aligned}
0_{10} & -255_{10} \\
00_{16} & --F_{16}
\end{aligned}
$$

- Byte $=2$ hexadecimal (hex) or base 16 digits
- Base-16 number representation
- Use characters ' 0 ' to ' 9 ' and ' $A$ ' to ' $F$ '
- Write FA1D37B ${ }_{16}$ in C
- as 0xFA1D37B or 0xfald37b

| $\text { aet }^{e^{c i n}} \sin ^{n^{2}} a^{r r}$ |  |  |
| :---: | :---: | :---: |
| 0 | 0 | 0000 |
| 1 | 1 | 0001 |
| 2 | 2 | 0010 |
| 3 | 3 | 0011 |
| 4 | 4 | 0100 |
| 5 | 5 | 0101 |
| 6 | 6 | 0110 |
| 7 | 7 | 0111 |
| 8 | 8 | 1000 |
| 9 | 9 | 1001 |
| A | 10 | 1010 |
| B | 11 | 1011 |
| C | 12 | 1100 |
| D | 13 | 1101 |
| E | 14 | 1110 |
| F | 15 | 1111 |

## What is memory, really?

- How do we find data in memory?


## Byte-Oriented Memory Organization



- Programs refer to addresses
- Conceptually, a very large array of bytes
- System provides an address space private to each "process"
- Process = program being executed + its data + its "state"
- Program can clobber its own data, but not that of others
- Clobbering code or "state" often leads to crashes (or security holes)
- Compiler + run-time system control memory allocation
- Where different program objects should be stored
- All allocation within a single address space


## Machine Words

■ Machine has a "word size"

- Nominal size of integer-valued data
- Including addresses
- Most current machines use 32 bits (4 bytes) words
- Limits addresses to 4GB
- Becoming too small for memory-intensive applications
- High-end systems use 64 bits (8 bytes) words
- Potential address space $\approx 1.8 \times 10^{19}$ bytes
- x86-64 machines support 48-bit addresses: 256 Terabytes
- Can't be real physical addresses -> virtual addresses
- Machines support multiple data formats
- Fractions or multiples of word size
- Always integral number of bytes


## Word-Oriented Memory Organization

■ Addresses specify locations of bytes in memory

- Address of first byte in word
- Addresses of successive words differ by 4 (32-bit) or 8 (64-bit)
- Address of word $0,1, . .10$ ?

| 64-bit <br> Words | 32-bit <br> Words | Bytes | Addr. |
| :---: | :---: | :---: | :---: |
|  |  |  | 0000 |
|  | Addr |  | 0001 |
|  | 0000 |  | 0002 |
| Addr |  |  | 0003 |
| = |  |  | 0004 |
|  | Addr |  | 0005 |
|  | 0004 |  | 0006 |
|  |  |  | 0007 |
|  |  |  | 0008 |
|  | Addr |  | 0009 |
|  | $=$ 0008 |  | 0010 |
| Addr $=$ |  |  | 0011 |
| 0008 |  |  | 0012 |
|  | Addr |  | 0013 |
|  | $=$ 0012 |  | 0014 |
|  |  |  | 0015 |

## Addresses and Pointers

- Address is a location in memory
- Pointer is a data object that contains an address

■ Address 0004
stores the value 351 (or $15 F_{16}$ )


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- Pointer to a pointer in 0024



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- Address 0004
stores the value 351 (or $\mathbf{1 5 F}_{16}$ )
- Pointer to address 0004 stored at address 001C
- Pointer to a pointer in 0024
- Address 0014
stores the value 12
- Is it a pointer?



## Data Representations

■ Sizes of objects (in bytes)

- Java Data Type
- boolean
- byte
- char
- short
- int
- float
- 
- double
- long
- 
- (reference)

C Data Type
bool
Typical 32-bit
x86-64
11
char 1
22
short int 2
int
4
float 4
4
long int 4
8
double 8
8
long long 8
$\begin{array}{lll}\text { long double } & 8\end{array}$
pointer * 4

## Byte Ordering

- How should bytes within multi-byte word be ordered in memory?
- Peanut butter or chocolate first?

■ Conventions!

- Big-endian, Little-endian
- Based on Guliver stories, tribes cut eggs on different sides (big, little)


## Byte Ordering Example

- Big-Endian (PPC, Internet)
- Least significant byte has highest address

■ Little-Endian (x86)

- Least significant byte has lowest address
- Example
- Variable has 4-byte representation 0x01234567
- Address of variable is $0 \times 100$



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- Variable has 4-byte representation 0x01234567
- Address of variable is $0 \times 100$




## Reading Byte-Reversed Listings

■ Disassembly

- Text representation of binary machine code
- Generated by program that reads the machine code
- Example instruction in memory
- add value 0x12ab to register 'ebx' (a special location in CPU's memory)

| Address | Instruction Code | Assembly Rendition |
| :--- | :--- | :--- |
| 8048366: | $81 \mathrm{c3}$ ab 120000 | add $\$ 0 \times 12 \mathrm{ab}$ \%ebx |

## Reading Byte-Reversed Listings

- Disassembly
- Text representation of binary machine code
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■ Example instruction in memory

- add value 0x12ab to register 'ebx' (a special location in CPU's memory)


## Address Instruction Code 8048366: 81 c3 ab 120000 <br> Assembly Rendition add \$0x12ab,\%ebx

Deciphering numbers
■ Value:

- Pad to 32 bits:

■ Split into bytes:
■ Reverse (little-endian):

## Addresses and Pointers in C

- Pointer declarations use *
- int * ptr; int x, y; ptr = \&x;
\& = 'address of value' * = 'value at address' or 'de-reference'
*(\&x) is equivalent to $x$
- Declares a variable ptr that is a pointer to a data item that is an integer
- Declares integer values named $x$ and $y$
- Assigns ptr to point to the address where x is stored
- We can do arithmetic on pointers
- ptr = ptr +1; // really adds 4 (because an integer uses 4 bytes)
- Changes the value of the pointer so that it now points to the next data item in memory (that may be $y$, may not - dangerous!)
■ To use the value pointed to by a pointer we use de-reference
- $y={ }^{*} p t r+1$; is the same as $y=x+1$;
- But, if ptr = \& y then $y=*$ ptr +1 ; is the same as $y=y+1$;
- *ptr is the value stored at the location to which the pointer ptr is pointing


## Arrays

- Arrays represent adjacent locations in memory storing the same type of data object
- E.g., int big_array[128]; allocated 512 adjacent locations in memory starting at 0x00ff0000
- Pointers to arrays point to a certain type of object
- E.g., int * array_ptr;
array_ptr = big_array;
array_ptr = \&big_array[0];
array_ptr = \&big_array[3];
array_ptr $=\& b i g \_a r r a y[0]+3$;
array_ptr $=$ big_array +3 ;
*array_ptr = *array_ptr + 1; array_ptr = \&big_array[130];
- In general: \&big_array[i] is the same as (big_array + i)
- which implicitly computes: \&bigarray[0] + i*sizeof(bigarray[0]);


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array_ptr = big_array; 0x00ff0000
array_ptr $=$ \&big_array[0]; $\quad 0 x 00 f f 0000$
array_ptr $=$ \&big_array[3]; $0 x 00 f f 000 \mathrm{c}$ array_ptr $=$ \&big_array[0] + 3; $\quad 0 x 00$ ff000c (adds 3 * size of int) array_ptr $=$ big_array $+3 ; \quad 0 x 00 f f 000 \mathrm{c}$ (adds 3 * size of int) *array_ptr $=$ *array_ptr $+1 ; \quad 0 x 00 \mathrm{ff000c}$ (but big_array $[3]$ is incremented) array_ptr = \&big_array[130]; 0x00ff0208 (out of bounds, $C$ doesn't check)
- In general: \&big_array[i] is the same as (big_array + i)
- which implicitly computes: \&bigarray[0] + i*sizeof(bigarray[0]);


## General rules for $\mathbf{C}_{\text {(assignments) }}$

- Left-hand-side = right-hand-side
- LHS must evaluate to a memory LOCATION
- RHS must evaluate to a VALUE (could be an address)

■ E.g., $x$ at location $0 x 04, y$ at $0 x 18$

- int $x, y$;
$x=y$; // get value at $y$ and put it in $x$



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- int $x, y$;
$x=y$; // get value at $y$ and put it in $x$
- int * $x$; int $y$;
$x=\& y+12 ; / /$ get address of $y$ add 12



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- LHS must evaluate to a memory LOCATION
- RHS must evaluate to a VALUE (could be an address)

■ E.g., $x$ at location $0 x 04, y$ at $0 x 18$

- int $x, y$;
$x=y$; // get value at $y$ and put it in $x$
- int * $x$; int $y$;
$x=\& y+3 ; / /$ get address of $y$ add 12
- int * $x$; int $y$;
* $x=y$; // value of $y$ to location $x$ points



## Examining Data Representations

■ Code to print byte representation of data

- Casting pointer to unsigned char * creates byte array

```
typedef unsigned char * pointer;
void show_bytes(pointer start, int len)
{
    int i;
    for (i = 0; i < len; i++)
    printf("0x%p\t0x%.2x\n", start+i, start[i]);
    printf("\n");
}
```

```
void show_int (int x)
{
    show_bytes( (pointer) &x, sizeof(int));
}
```

Some printf directives:
\%p: Print pointer
\%x: Print hexadecimal
"\n": New line

## show_bytes Execution Example

int a = 12345; // represented as $0 \times 00003039$ printf("int a = 12345; $\mathrm{n}^{2}$ ");
show_int(a); // show_bytes((pointer) \&a, sizeof(int));
Result (Linux):

| int $a=12345 ;$ |  |
| :--- | :--- |
| $0 x 11 f f f f c b 8$ | $0 \times 39$ |
| $0 x 11 f f f f c b 9$ | $0 x 30$ |
| $0 x 11 f f f f c b a$ | $0 x 00$ |
| $0 x 11 f f f f c b b$ | $0 x 00$ |

## Representing Integers

- int $A=12345$;

■ int $B=-12345$;

- long int $C=12345$;

| FF |
| :---: |
| FF |
| CF |
| C 7 |

IA32, x86-64 A Sun A
IA32, x86-6

| 39 |
| :---: |
| 30 |
| 00 |
| 00 |

IA32, x86-64 B

| C 7 |
| :---: |
| CF |
| FF |
| FF |


| 00 |
| :--- |
| $\mathbf{0 0}$ |
| 30 |
| 39 |

Sun B

Decimal: 12345
Binary: 0011000000111001
Hex: $\quad 3 \quad 0 \quad 3 \quad 9$
IA32 C

| 39 |
| :---: |
| 30 |
| 00 |
| 00 |


| X86-64 C |
| :---: |
| 39 <br> 30 <br> 00 <br> 00 <br> 00$\quad$00 <br> 00 <br> 30 <br> 39 |

Two's complement representation for negative integers (covered later)

## Representing Integers

| IA32, x86-64 A | Sun A |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| 39 | 00 | IA32 C | X86-64 C | Sun C |
| 30 | 00 | 39 | 39 | 00 |
| 00 | 30 | 30 | 30 | 00 |
| 00 | 39 | 00 | 00 | 30 |
|  |  | 00 | 00 | 39 |
| IA32, x86-64 B | Sun B |  | 00 |  |
| C7 | FF |  | 00 |  |
| CF | FF |  | 00 |  |
| FF | CF |  | 00 |  |
| FF | C7 | wo's c | nt repres |  |
|  |  | for nega | gers (cov |  |


| IA32, x86-64 A | Sun A |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| 39 | 00 | IA32 C | X86-64 C | Sun C |
| 30 | 00 | 39 | 39 | 00 |
| 00 | 30 | 30 | 30 | 00 |
| 00 | 39 | 00 | 00 | 30 |
|  |  | 00 | 00 | 39 |
| IA32, x86-64 B | Sun B |  | 00 |  |
| C7 | FF |  | 00 |  |
| CF | FF |  | 00 |  |
| FF | CF |  | 00 |  |
| FF | C7 | wo's c | nt repres |  |
|  |  | for nega | gers (cov |  |


| IA32, x86-64 A | Sun A |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| 39 | 00 | IA32 C | X86-64 C | Sun C |
| 30 | 00 | 39 | 39 | 00 |
| 00 | 30 | 30 | 30 | 00 |
| 00 | 39 | 00 | 00 | 30 |
|  |  | 00 | 00 | 39 |
| IA32, x86-64 B | Sun B |  | 00 |  |
| C7 | FF |  | 00 |  |
| CF | FF |  | 00 |  |
| FF | CF |  | 00 |  |
| FF | C7 | wo's c | nt repres |  |
|  |  | for nega | gers (cov |  |

- int $A=12345$;

■ int $B=-12345$;

- long int $C=12345$;

IA32, x86-64 A Sun A

IA32, x86-64 B
Sun B

Decimal: 12345
Binary: 0011000000111001
Hex: $\quad 3 \quad 0 \quad 3 \quad 9$

## Representing Integers

- int $A=12345$;

■ int $B=-12345$;

- long int $C=12345$;
IA32, x86-64 A Sun A

| 39 |
| :---: |
| 00 |
| 30 |

IA32, x86-64 B Sun B


## Decimal: 12345

Binary: 0011000000111001
Hex: 30030
IA32 C

| 39 |
| :---: |
| 30 |
| 00 |
| 00 |

X86-64 C

| 39 |
| :---: |
| 30 |
| 00 |
| 00 |
| 00 |
| 00 |
| 00 |
| 00 |

Sun C

| 00 |
| :--- |
| 00 |
| 30 |
| 39 |

Two's complement representation for negative integers (covered later)

## Representing Integers

- int $A=12345$;

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IA32, x86-64 A Sun A

IA32, x86-64 B Sun B
IA32, x86-64 A Sun A


## Decimal: 12345

Binary: 0011000000111001
Hex: 30030

| IA32 C |
| :---: |
| 39 |
| 30 |
| 00 |
| 00 |

Sun C

| 00 |
| :--- |
| 00 |
| 30 |
| 39 |

Two's complement representation for negative integers (covered later)

## Representing Integers

- int $A=12345$;

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IA32, x86-64 A Sun A

| 39 |
| :---: |
| 00 |
| 00 |

IA32, x86-64 B Sun B


## Decimal: 12345

Binary: 0011000000111001
Hex: $3 \quad 0 \quad 3 \quad 9$
IA32 C

Two's complement representation for negative integers (covered later)

## Representing Pointers

- int $\mathrm{B}=-12345$;
- int *P $=\& B$;

| Sun P | IA32 P | x86-64 P |
| :---: | :---: | :---: |
| EF | D4 | OC |
| FF | F8 | 89 |
| FB | FF | EC |
| 2C | BF | FF |
|  |  | FF |
|  |  | 7F |
|  |  | 00 |
|  |  | 00 |

Different compilers \& machines assign different locations to objects

## Representing strings

- A C-style string is represented by an array of bytes.
- Elements are one-byte ASCII codes for each character.
- A 0 value marks the end of the array.

| 32 | space |
| :---: | :---: |
| 33 | $!$ |
| 34 | $"$ |
| 35 | $\#$ |
| 36 | $\$$ |
| 37 | $\%$ |
| 38 | $\&$ |
| 39 | , |
| 40 | $($ |
| 41 | $)$ |
| 42 | $*$ |
| 43 | + |
| 44 | , |
| 45 | - |
| 46 | $;$ |
| 47 | 1 |


| 48 | 0 |
| :--- | :--- |
| 49 | 1 |
| 50 | 2 |
| 51 | 3 |
| 52 | 4 |
| 53 | 5 |
| 54 | 6 |
| 55 | 7 |
| 56 | 8 |
| 57 | 9 |
| 58 | $:$ |
| 59 | $;$ |
| 60 | $<$ |
| 61 | $=$ |
| 62 | $>$ |
| 63 | $?$ |


| 64 | @ |
| :--- | :--- |
| 65 | A |
| 66 | B |
| 67 | C |
| 68 | D |
| 69 | E |
| 70 | F |
| 71 | G |
| 72 | H |
| 73 | I |
| 74 | J |
| 75 | K |
| 76 | L |
| 77 | M |
| 78 | N |
| 79 | O |


| 80 | P |
| :--- | :--- |
| 81 | Q |
| 82 | R |
| 83 | S |
| 84 | T |
| 85 | U |
| 86 | V |
| 87 | W |
| 88 | X |
| 89 | Y |
| 90 | Z |
| 91 | [ |
| 92 | I |
| 93 | ] |
| 94 | $\wedge$ |
| 95 | - |

$\left.\begin{array}{|ll|}\hline 96 & \\ 97 & \mathrm{a} \\ 98 & \mathrm{~b} \\ 99 & \mathrm{c} \\ 100 & \mathrm{~d} \\ 101 & \mathrm{e} \\ 102 & \mathrm{f} \\ 103 & \mathrm{~g} \\ 104 & \mathrm{~h} \\ 105 & \mathrm{l} \\ 106 & \mathrm{j} \\ 107 & \mathrm{k} \\ 108 & \mathrm{l} \\ 109 & \mathrm{~m} \\ 110 & \mathrm{n} \\ 111 & \mathrm{o}\end{array}\right]$

| 112 | p |
| :---: | :---: |
| 113 | q |
| 114 | r |
| 115 | s |
| 116 | t |
| 117 | u |
| 118 | v |
| 119 | w |
| 120 | x |
| 121 | y |
| 122 | z |
| 123 | \{ |
| 124 | l |
| 125 | $\}$ |
| 126 | $\sim$ |
| 127 | del |

## Null-terminated Strings

- For example, "Harry Potter" can be stored as a 13-byte array.

| 72 | 97 | 114 | 114 | 121 | 32 | 80 | 111 | 116 | 116 | 101 | 114 | 0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| H | a | $r$ | $r$ | y |  | P | 0 | t | t | e | $r$ | $\backslash 0$ |

- Why do we put a a 0 , or null, at the end of the string?
- Computing string length?


## Compatibility

char S[6] = "12345";

| Linux/Alp | Sun S |
| :---: | :---: |
| 31 | 31 |
| 32 | 32 |
| 33 | 33 |
| 34 | 34 |
| 35 | 35 |
| 00 | 00 |

- Byte ordering not an issue

■ Unicode characters - up to 4 bytes/character

- ASCII codes still work (leading 0 bit) but can support the many characters in all languages in the world
- Java and C have libraries for Unicode (Java commonly uses 2 bytes/char)


## Boolean Algebra

- Developed by George Boole in 19th Century
- Algebraic representation of logic
- Encode "True" as 1 and "False" as 0
- AND: $A \& B=1$ when both $A$ is 1 and $B$ is 1
- OR: $A \mid B=1$ when either $A$ is 1 or $B$ is 1
- XOR: $A^{\wedge} B=1$ when either $A$ is 1 or $B$ is 1 , but not both
- NOT: $\sim A=1$ when $A$ is 0 and vice-versa
- DeMorgan’s Law: ~(A | B $)=\sim$ A \& ~B

$$
\begin{array}{l|ll}
\& & 0 & 1 \\
\hline 0 & 0 & 0 \\
1 & 0 & 1
\end{array}
$$




## General Boolean Algebras

- Operate on bit vectors
- Operations applied bitwise

| 01101001 | 01101001 | 01101001 |  |  |
| ---: | ---: | ---: | ---: | ---: |
| $\& \quad 01010101$ |  | 01010101 |  | 01010101 |

■ All of the properties of Boolean algebra apply

$$
\begin{array}{r}
01010101 \\
\times 01010101 \\
\hline
\end{array}
$$

■ How does this relate to set operations?

## Representing \& Manipulating Sets

■ Representation

- Width $w$ bit vector represents subsets of $\{0, \ldots, w-1\}$
- $\mathrm{a}_{j}=1$ if $j \in A$

01101001
76543210

01010101
76543210
■ Operations

- \& Intersection
$01000001\{0,6\}$
- | Union
- ^ Symmetric difference
$01111101\{0,2,3,4,5,6\}$
$00111100\{2,3,4,5\}$
- ~ Complement


## Bit-Level Operations in C

■ Operations \& , $1, \wedge$, ~ are available in C

- Apply to any "integral" data type
- long, int, short, char, unsigned
- View arguments as bit vectors
- Arguments applied bit-wise
- Examples (char data type)
- ~0x41 --> 0xBE
~010000012 --> $10111110_{2}$
- ~0x00 --> 0xFF
~000000002 --> 111111112
- 0x69 \& 0x55 --> 0x41
$01101001_{2} \& 01010101_{2}$--> $01000001_{2}$
- 0x69 | 0x55 --> 0x7D
$01101001_{2}$ | $01010101_{2}$--> $01111101_{2}$


## Contrast: Logic Operations in C

■ Contrast to logical operators

- \& \& , | |, !
- View 0 as "False"
- Anything nonzero as "True"
- Always return 0 or 1
- Early termination

■ Examples (char data type)

- ! 0x41 --> 0x00
- ! 0x00 --> 0x01
- ! ! 0x41 --> 0x01
- $0 \times 69$ \&\& $0 \times 55$--> $0 \times 01$
- 0x69 || $0 \times 55$--> $0 \times 01$
- $p$ \& \& $\mathrm{p}++\quad$ (avoids null pointer access, null pointer $=0 \times 00000000$

