

CSE352 Spring 2013 Homework Assignment #4
Due in class Wednesday, May 15 2013

Q1) In the diagram below both X and Y are n-bit wide registers. At each clock cycle you can select a bit-wise basic operation between X and Y and load it to either X or Y, while the other register keeps its value. Exchange the contents of X and Y. (The solution would be a sequence of XnotY and logic-op selections).

