

CSE352 Autumn 2014 Homework #2

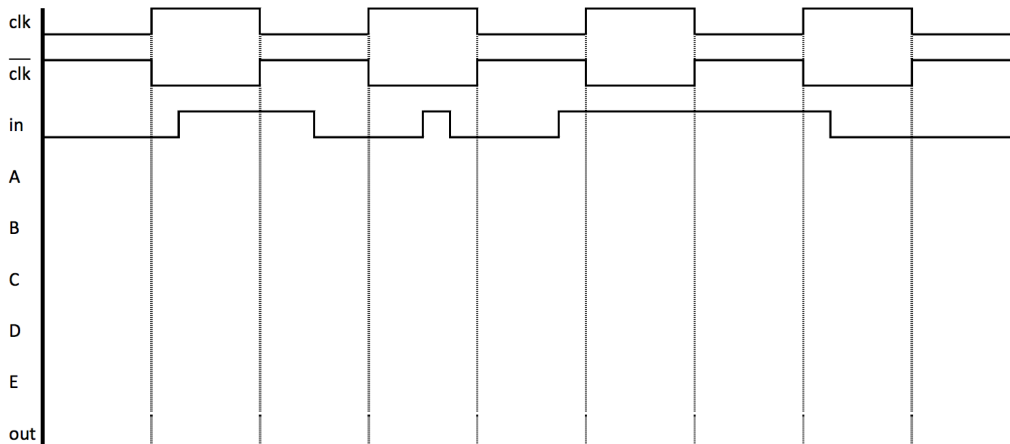
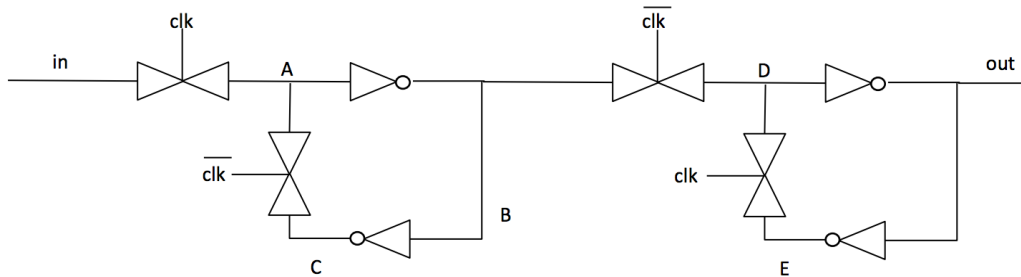
Due In Class Friday 10/17/2014

Please write your name and student ID at the top right corner of each page, and staple or paperclip your work together. We are NOT responsible for losing papers that were not stapled or paperclipped together.

Complete the following questions. Please write legibly and try to draw clean diagrams. Spaghetti wiring in circuit diagrams is difficult to grade. We will not grade work that is too heavily encrypted for us to read (i.e. we can't read it, we can't grade it). Please consider typesetting your work if you think that it may not be legible to the grader. This is an individual assignment. You may not work in groups. Justice will be enforced if you are caught cheating.

Fill in the chart above with the appropriate signals. Mark X's in regions where the signal state is not known.

Is this a falling edge or rising edge flip flop?



Fill in the chart above with the appropriate signals. Mark X's in regions where the signal state is not known.

Is this a falling edge or rising edge flip flop?

