



Alyssa obviously disagrees and thinks that there is an implementation that has a delay of  $7n$ . Does such an implementation exist? If so show how it can be done.

**Problem 2** *Blast from the Past (Review)*

Draw the CMOS transistor implementation of the following Boolean expressions. Minimize the number of transistors used:

(a)  $\overline{(A + B)(DC + EC)}$

(b)  $\overline{A} \overline{B} + \overline{C} + \overline{D}$