

Solutions to Assignment 8.

1.A. Let Priority A be represented by 0, and Priority B by 1.

The next state table:

Q	R_A	R_B	Q^+	G_A	G_B
0	0	0	1	0	0
0	0	1	1	0	1
0	1	0	0	1	0
0	1	1	0	1	0
1	0	0	0	0	0
1	0	1	1	0	1
1	1	0	0	1	0
1	1	1	1	0	1

Q	$R_A R_B$	00	01	11	10
Q^+	0	1	1	0	0
Q	1	0	0	1	1

$$Q^+ = \overline{Q} \bar{R}_A + Q R_B$$

$$= \overline{Q + R_A} + Q R_B$$

Q	$R_A R_B$	00	01	11	10
Q	0	1	1	0	0
Q	1	0	0	1	1

G_A :

$$G_A = \overline{Q} R_A + R_A \bar{R}_B$$

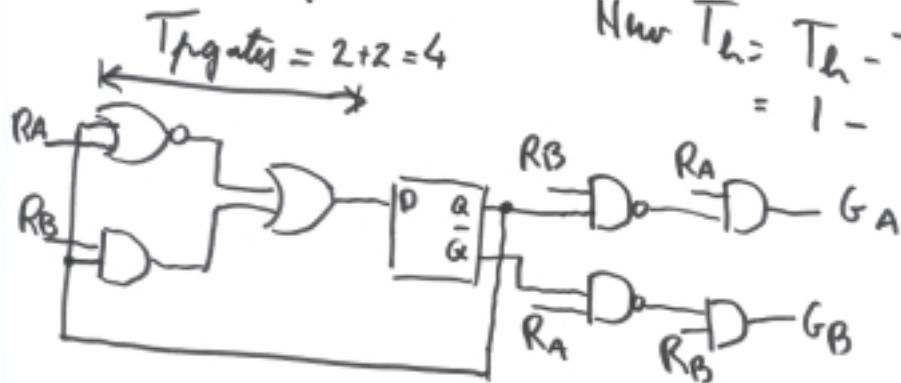
$$= R_A (\overline{Q} R_B)$$

Q	$R_A R_B$	00	01	11	10
Q	0	1	1	0	0
Q	1	0	0	1	1

$$G_B = \overline{R}_A \overline{R}_B + Q R_B$$

$$= R_B \overline{Q} R_A$$

The diagram:



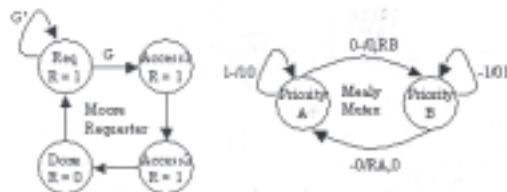
$$\text{New } T_{su} = T_{su} + T_{\text{gate}} = 2 + 4 \\ = 6$$

$$\text{New } T_h = T_h - T_{\text{gate}} \\ = 1 - 4 = -3$$

B. See attached paper for timing diagram.

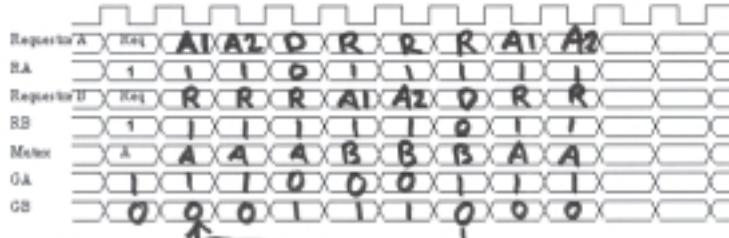
Reading: Katz 8 except 8.3, 9 except 9.3.4, 10.2, and 11 for reference.

Problem 1.
 Moore v Mealy, Communicating State Machines:



Part A. Complete the design of the Mealy Mutex state machine above using D-flip-flops. If $T_{su} = 2\text{ms}$ and $T_h = 1\text{ms}$ for the D-ff, what are the setup and hold times for RA and RB?

Part B. Without considering detailed timing issues, draw the timing diagram for a system consisting of two instances of the Moore Requester and one Mealy Mutex. Your timing diagram should show all of the signal values, and the states of all three machines. The initial states are shown. What is the utilization of the hypothetical resource (Access Cycles/Total Cycles)?



Problem 2.
 State Machine Analysis: Katz 8.7

Problem 3.
 State Minimization: Katz 9.5

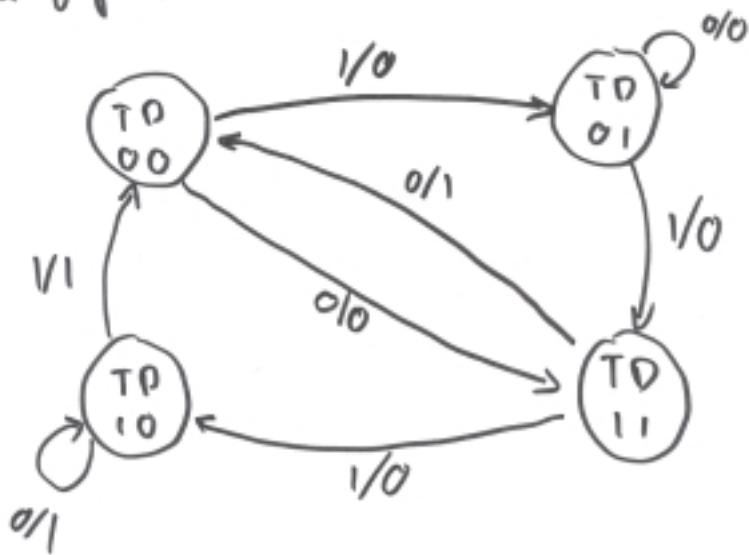
← →
 6 cycles out of which
 the resources is used
 in 4 cycles \Rightarrow utilization = $\frac{4}{6} = \frac{2}{3}$

$$2. \text{ Note: } D^+ = \bar{T} \quad Z = (\overline{D_I}) T$$

$$T^+ = I \text{ XNOR } D$$

Also Note that $A \text{ XNOR } 0 = \bar{A}$
and $A \text{ XNOR } 1 = A$

So, simply draw the four states, and
then figure out the arrows:



3. Next state table:

Current	Next		Output
$X=0$	$X=1$		

S_0	S_5	S_2	0
S_1	S_0	S_1	1
S_2	S_0	S_5	1
S_3	S_3	S_1	1
S_4	S_0	S_5	1
S_5	S_0	S_5	1
S_6	S_5	S_4	0

S_1	$\cancel{S_0-S_0}$	$\cancel{S_0-S_3}$	$\cancel{S_0-S_3}$	$\cancel{S_0-S_3}$	$\cancel{S_0-S_3}$
S_2	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$
S_3	$\cancel{S_0-S_3}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$
S_4	$\cancel{S_0-S_0}$	$\cancel{S_0-S_0}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$
S_5	$\cancel{S_0-S_0}$	$\cancel{S_0-S_0}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$
S_6	S_5-S_5	$\cancel{S_2-S_4}$	$\cancel{S_0-S_3}$	$\cancel{S_1-S_5}$	$\cancel{S_0-S_3}$
	S_0	S_1	S_2	S_3	S_4

Thus, equivalent states are:

$$S_0 = S_6$$

$$S_1 = S_7 = S_9 = S_5$$

Let $S_{1245} = S_1 = S_2 = S_4 = S_5$

$$S_{06} = S_0 = S_6$$

The new state diagram:

