

Data Path Structure
add $\$ 6, \$ 7, \$ 2$

|  |  |  |  |  |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| 4 | memory $-\square$ | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 | C |
|  | 0 | $\square$ | $\square$ | $\square$ | 0 | $\square$ | $\square$ | 1bit ALU |  |  |



1Bit ALU: Logic Operations $\qquad$


Bitwise Logic Operations AND \$result,\$a,\$b OR \$result,\$a,\$b

a


LOGIC-OPs: $\operatorname{MUX}(\mathrm{op}, \operatorname{AND}(\mathrm{a}, \mathrm{b}), \operatorname{OR(a,b))}$

## Logic for Carry Out



Cout $=(\mathrm{b} \cdot \operatorname{Cin})+(\mathrm{a} \cdot \operatorname{Cin})+(\mathrm{a} \cdot \mathrm{b})+(\mathrm{a} \cdot \mathrm{b} \cdot \operatorname{Cin})$ Cout: $\operatorname{OR}(\operatorname{AND}(\mathrm{b}, \operatorname{Cin}), \operatorname{AND}(\mathrm{a}, \operatorname{Cin}), \operatorname{AND}(\mathrm{a}, \mathrm{b}))$

Cout: $O R(A N D(\mathrm{~b}, \mathrm{Cin}), \operatorname{AND}(\mathrm{a}, \operatorname{Cin}), A N D(\mathrm{a}, \mathrm{b}))$

$$
\text { Cout }=(\mathrm{b} \cdot \operatorname{Cin})+(\mathrm{a} \cdot \operatorname{Cin})+(\mathrm{a} \cdot \mathrm{~b})+(\mathrm{a} \cdot \mathrm{~b} \cdot \operatorname{Cin})
$$




