

## Topics

### ILP & TLP

### Multiprocessors

- Cache coherency on bus-based & distributed MIMDs (see the end of the coherency lecture)
- Synchronization (see the lists at the end of the synchronization lecture)
- 3 models (programming, execution, taxonomy)

### Multithreaded processors

- Traditional vs. SMT (see the lists at the end of the multithreading lecture)

### Dataflow machines

- See the lists at the end of the WaveScalar lecture

## Themes Throughout Different Designs

Different kinds of parallelism and how to achieve them

Uses of snooping

Distributed implementations

Mechanisms for increasing throughput of something

Motivation for the different designs

How different processor designs achieved their goals

Why different designs work well

Different scenarios for speculation

State bits for cache blocks