Software

Compilers

- assemblers
- I high-level language compilers
- I loaders/linkers

Layers of abstraction

- subroutines
- device drivers
- I run-time systems: concurrency and communication
- I operating systems
- I application programmer interfaces (APIs)

Debugging

- emulation
- monitors
- agents

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Writing code

Machine code

Assembler

- macro capability
- symbolic variables
- I direct access to microcontroller's special features

Compilers

- high-level language (C, C++, etc.)
- I escape to assembly language when needed
- I API/header files for special features (I/O ports, special registers, etc.)

Program loader/linker

- stores program into ROM/RAM
- combines multiple programs (e.g., user code with device drivers)

Abstraction

Subroutines

- I encapsulate frequently used functions
- I save system state on entering, restore on exit
- I parameter passing through the stack or registers

Device drivers

- I special subroutines for accessing peripheral devices
- may or may not include state
- I interactions with other activities (e.g., interrupts)

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Very simple device driver

- Turn LED on/off
- Parameters:
 - | port pin
- API:
 - on(port_pin) specifies the port pin (e.g., port D pin 3)
 - off(port_pin)
- Interactions:
 - I only if other devices want to use the same port

Simple device driver

- Turning an LED on and off at a fixed rate
- Parameters:
 - | port pin
 - I rate at which to blink LED
- API:
 - on(port_pin, rate)
 - specifies the port pin (e.g., port D pin 3)
 - specifies the rate to use in setting up the timer (what scale?)
 - off(port_pin)
- Internal state and functions:
 - I keep track of state (on or off for a particular pin) of each pin
 - I interrupt service routine to handle timer interrupt
 - I set up interrupt service routine address

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Interesting interactions

- What if other devices also need to use timer (e.g., PWM device)?
 - I timer interrupts now need to be handled differently depending on which device's alarm is going off
- Benefits of special-purpose output compare peripheral
 - I output compare pins used exclusively for one device
 - each output compare has a separate interrupt handling routine
- What if we don't have output compare capability?

Sharing timers

- Create a new device driver for the timer unit
 - I allow other devices to ask for timer services
 - manage timer independently so that it can service multiple requests

Parameters:

I time to wait, address to call when timer reaches that value

API:

- set_timer(time_to_wait, call_back_address)
 - set call_back_address to correspond to time+time_to_wait
 - compute next alarm to sound and set up timer for that
 - update in interrupt service routine for next alarm

Internal state and functions:

- I how many alarms can the driver keep track of?
- I how are they organized? FIFO? priority queue?

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Concurrency

- Multiple programs interleaved to as if parallel
- Each program requests access to devices/services
 - e.g., timers, serial ports, etc.

Exclusive or concurrent access to devices

- allow only one program at a time to access a device (e.g., serial port)
- arbitrate multiple accesses (e.g., timer)

State and arbitration needed

- keep track of state of devices and concurrent programs using resource
- I arbitrate their accesses (order, fairness, exclusivity)
- monitors/locks (supported by primitive operations in ISA test-and-set)

Interrupts

- I disabling may effect timing of programs
- I keeping enabled may cause unwanted interactions

Handling concurrency

Traditional operating system

- I multiple threads or processes
- I file system
- virtual memory and paging
- input/output (buffering between CPU, memory, and I/O devices)
- I interrupt handling (mostly with I/O devices)
- I resource allocation and arbitration
- I command interface (execution of programs)

Embedded operating system

- I lightweight threads
- input/output
- I interrupt handling
- I real-time guarantees

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Embedded operating systems

Lightweight threads

- I basic locks
- I fast context-switches

Input/output

- API for talking to devices
- buffering

Interrupt handling (with I/O devices and UI)

- I translate interrupts into events to be handled by user code
- I trigger new tasks to run (reactive)

Real-time issues

- I guarantee task is called at a certain rate
- guarantee an interrupt will be handled within a certain time
- I priority or deadline driven scheduling of tasks

Examples

- Palm OS (e.g., IBM Workpad)
 - US Robotics Palm Pilot
 - Motorola microcontrollers (68328 Dragonball)
 - simple OS for PDAs
 - I only supports single threads
- Windows CE (e.g., Nino)
 - I PDA operating system
 - spin-off of Windows '95
 - I portable to a wide variety of processors
 - I full-featured OS modularized to only include features as needed

embedded operating

systems typically reside in ROM (flash)

- Wind River Systems VxWorks
 - I one of the most popular embedded OS kernels
 - I highly portable to an even wider variety of processors (tiny to huge)
 - I modularized even further

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Palm OS

- Interface management
 - I basic user interface model
 - l event dispatch loop paradigm
- System management
 - I alarm and time (timers and real-time clocks)
 - sound, pen, key, serial port (I/O devices)
 - string (libraries of routines)
 - system (errors, power, application start/stop)
 - event (interrupt->event translation, dispatch loop)
- Memory management
 - I structures for data in memory (heaps, records, databases)
- Communication
 - serial port (layered with TCP/IP protocol)

Palm OS Application Model

One application is "running"

- I running application has control of the screen and user input
- I others may be active in the background and receive other I/O events
- I when application starts, "PilotMain" is called

Main

- I runs "AppStart" to set up user interface of program
- I runs "AppStop" before it stops execution
- all data in RAM
- I permanent storage in memory heaps organized in "databases"
- "AppEventLoop" is run between start and stop

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PilotMain

Events

- Generated by system (timers, I/O, user input, etc.)
- Applications polls (in a loop) for new events
- Many different types of events (29 in all)
 - application events (e.g., stop)
 - user interface events
 - menu
 - selection
 - pen
 - I key

Interpret events

- specific to application
- I default behavior provided by system

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Event dispatch loop

- Get event
- Handle event
 - system event handler
 - menu event handler
 - I application event handler
 - user interface element event handler
- Leverage default behaviors
 - I most user code goes into the application event handler

AppEventLoop

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Serial Line

Serial Line (cont'd)

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Serial Line (cont'd)

```
SerReceive(serRefNum, serRcvQueue, 1, 0, &err);
if (err == serErrTimeOut) return;
```

Storage system

- Everything is stored in RAM
 - I dynamic (stacks, heaps, global vars of application)
 - when application stops, data may be lost
 - storage (databases)
 - analogous to a file and provides static storage across invocation of the application
- Databases
 - I chunks of data
 - leach entry is associated with a type (linked to an application)

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Conduits

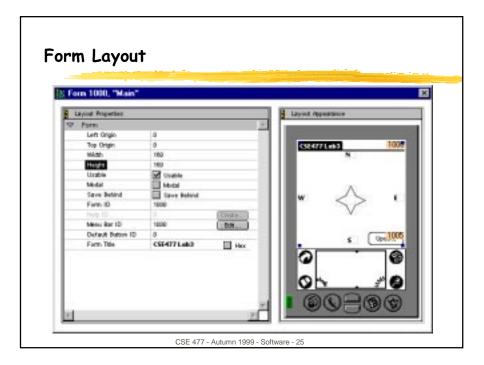
- Mechanism for transferring databases across serial ports of device (RS232 or IrDA) to PC
- Hot-Sync
 - I synchronizes RAM databases with PC copies
 - I changes on PC propagate to RAM and vice-versa
- Also used to transfer new applications to RAM
- Can be extended to bring arbitrary data into the Pilot
 - I e.g., collect e-mail for later reading
- Pilot was envisioned as extension of desktop
 - user input at both ends (PC or Pilot)
 - I same data (synchronized periodically)

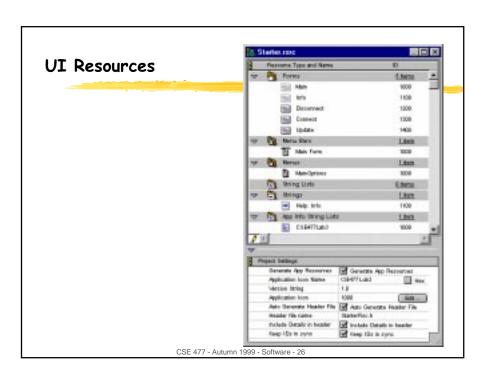
Development environment

- Elements
 - user interface constructor
 - I source code editor
 - compiler
 - debugger
- SDKs: system development kits
 - Metrowerks CodeWarrior for Palm Pilot
 - I GNU for Palm Pilot
- Conduit development kits for PC

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User Interfaces in PalmOS CSE 477 - Autumn 1999 - Software - 24





Resources header file

```
// Resource: tFRM 1000
#define MainForm
                                      1000 //(Left Origin = 0, Top Origin = 0, Width =
#define MainUpdateButtonButton 1005 //(Left Origin = 107, Top Origin = 138, Widt
#define MainUnnamed1001BitMap
                                     1000 //(Left Origin = 72, Top Origin = 67, Bitmag
                                     1200 //(Left Origin = 86, Top Origin = 82, Bitma
#define MainUnnamed1002BitMap
#define MainUnnamed1003BitMap
                                     1100 //(Left Origin = 72, Top Origin = 95, Bitmag
                                     1300 //(Left Origin = 57, Top Origin = 82, Bitmag
#define MainUnnamed1004BitMap
                                     1006 //(Left Origin = 72, Top Origin = 15, Usable
1007 //(Left Origin = 75, Top Origin = 144, Usabl
1008 //(Left Origin = 145, Top Origin = 80, Usabl
#define MainUnnamed1006Label
#define MainUnnamed1007Label
#define MainUnnamed1008Label
#define MainUnnamed1009Label 1009 //(Left Origin = 0, Top Origin = 80, Usable
// Resource: tFRM 1100
#define InfoForm
                                     1100 //(Left Origin = 2, Top Origin = 46, Width
#define InfoUnnamed1101Button 1101 //(Left Origin = 60, Top Origin = 75, Width #define InfoUnnamed1102Label 1102 //(Left Origin = 45, Top Origin = 29, Usable
// Resource: MENU 1000
                                                           1000
#define MainOptionsMenu
#define MainOptionsConnect
                                                           1000
#define MainOptionsDisconnect
#define MainOptionsUpdate
#define MainOptionsAboutCSE477Lab3
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```

Debugging code

Emulators

- I replaces microcontroller in system
- "debuggable" version of microcontroller

Monitors

- I add code to microcontroller that can always take control
- requires resources

Agents

- I smaller amount of code called by program being debugged
- I does not provide complete control

Debugging code: direct

Real microcontroller

- I hard limits (e.g., program must fit in available memory of target)
- I load ROM or dual-ported RAM with program
- I initializations must be just right (e.g., stack pointer and comm port)
- I debugged via logic analyzer on pins
- I modern microcontrollers include special pins to permit access to internal state without disrupting running program

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Debugging code: emulation

Emulator

- I replaces microcontroller in target system being designed
- pin-compatible (timing also, although not always perfect)
- I provides access to internal memory and registers (hard to get to otherwise due to limitations of I/O pins)
- single-step capability (with links to source code)
- I relaxes memory bounds (fakes external memory including "infinite" stack)

Debugging code: monitor

Monitor

- I adds small program to microcontroller code
- I usually in ROM inside microcontroller or on target board
- provides system initialization
- I runs user program as subroutine
- can always get control of program (via interrupts)
- I uses system resources (e.g., timer, serial line, LAN)
- I makes it difficult to debug device drivers and real-time code (e.g., OS)
- I provides many of the same functions as emulator at no hardware cost
- e.g., Angel monitor for StrongARM

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Debugging code: agents

Agents

- I similar to monitors in function
- I are called as subrouting by program being debugged
- | limits resources
- I not as robust (program may crash before calling agent)
- I usually requires OS support and run as concurrent process