CSE584: Software Engineering Lecture 3: Requirements & Specification (B)

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Last week & this week

· Last week

- Overview - Program correctness
- Model-based specifications (Z)
- Intro to state machines
- · This week

- Analysis of state machine based specifications (model checking)
- Michael Jackson on video: "The World and the Machine"
- Some wrap up

Before that...

- Last week I was at a workshop on highly dependable computing systems At NASA Ames Research Center
 - Academia, government, industry
- IBM, Sun, Oracle, Sybase, Microsoft, Boeing, Honeywell,
- · Keynotes, case studies, breakout sessions, etc.
- Dependability is different things to different people
- Over all. I think that there were two camps
 - Use technology to improve dependability
 - Build a "culture of dependability"

Two NASA failures: each over \$100 million

http://www.nasa.gov/newsinfo/marsreports.html

- · Mars Climate Orbiter
 - A confusion in metric/English units caused an engine to fire too strongly, bringing the spacecraft too close to Mars and causing it to crash instead of orbitina
- · Polar lander (very probable cause) At 40m above Mars, a parachute and spring-loaded legs were deployed and a new control regime was used as planned
 - The spring-loaded legs bounced, causing the regime to think that the pads had hit the surface
 - The engine was turned off and the spacecraft
 - crashed

Specification errors?

Not the units

- The specification was completely clear about this A new programmer didn't know or check, and used the wrong units

- Not caught by testing, inspections, etc. Tricky to catch by testing, since it was a second order effect
- What can be done about errors like these?
- Polar lander? Unclear
- Each module (regime) worked as specified
 The < 40m module assumed that a variable would be in a particular state upon entry, but it wasn't due to the leg bounce
- What this a problem in the inter-module specification? In the implementation of the <40m module? Testing? Something else?

Specifications thread

- I found it interesting to come back from this workshop and see the thread on the mailing list about "spec avoidance"
- Specs would surely help solve some perhaps many - of your problems - But not all
 - And the cost is not clear
 - _
- I'll note that most of you wanted specs, but didn't necessarily want the responsibility of writing them · See the paper that Cordell Green mentioned, which I posted on the mailing list

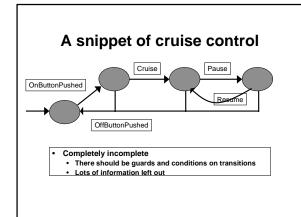
State machines

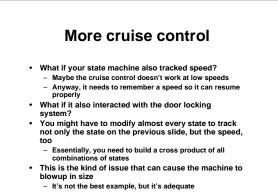
- Good for specifying reactive systems, protocols, etc.
 Event-driven
 - External events (actions in the external environment, such as "button pushed", "door opened", "nuclear core above safe temperature", etc.)
 - above safe temperature", etc.) Internal events (actions defined in the internal system to cause needed actions)
 - Can generate external events that may drive actuators in the environment (valves may be opened, alarms may be
 - Transitions can have guards and conditions that control whether or not they are taken
- "Flat" (non-hierarchical) state machines tend to
- explode in size relatively quickly

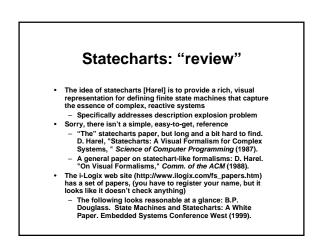
Classic examples

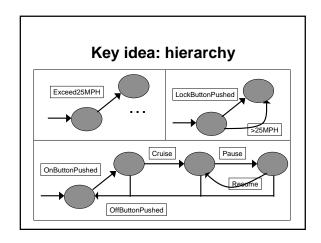
- · Specifying a cruise control
- Specifying the traffic lights at an intersection
- Specifying trains on shared tracks

 Could be managing the bus tunnel in Seattle
- Etc.









Parallel AND-machines

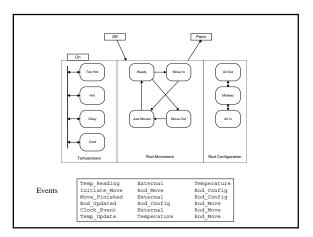
- The state of the overall machine is represented by one state from each of the parallel AND machines
 - In a cruise control state AND in a speed state AND in a door lock state
- Transitions can take place in all substates in parallel
- Events in one substate can cause transitions in another substate

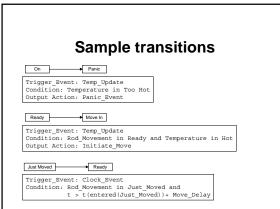
A few statechart features

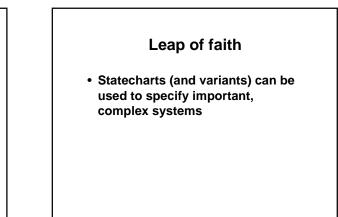
- Default entry states for each substate – Indicated by an arrow with no initial state
- When any of the parallel machines is exited, the entire machine is exited
- You can have "history" states, which remember where you were the last time you were in a machine
- The "STATEMATE semantics" are the standard semantics
 - This is largely a question of which enabled transitions are taken, and when
 - At this level, you surely don't care

Variants on statecharts

- There are many variants on statecharts
- One is RSML (Leveson et al.), which allows states to be connected through a bus as well as pairwise
- RSML also represents transitions differently, through explicit AND-OR tables instead of through guards and conditions on transitions







Question

- So we have a big statecharts-like specification
- How do we know it has properties we want it to have?
 - Ex: is it deterministic?
 - Ex: can you ever have the doors unlock by themselves while the car is moving?
 - Ex: can you ever cause an emergency descent when you are under 500 feet above ground level?

Standard answers include

- Human inspection
- Simulation
- Analysis

An alternative: model checking

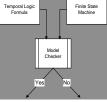
 Evaluate temporal properties of finite state systems

 Guarantee a property is true or return a counterexample
 Ex: Is it true that we can never enter an error state?
 Ex: Are we able to handle a

Ex: Are we able to handle a reset from any state?
 Extremely successfully for hardware verification

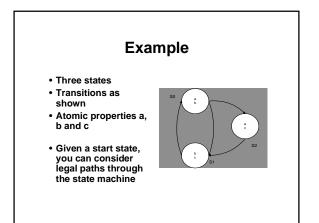
 Intel got into the game after the FDIV error

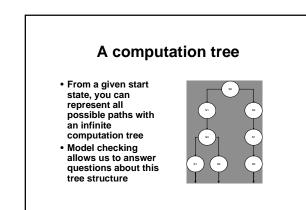
 Open question: applicable to software specifications?



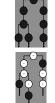
State Transition Graph

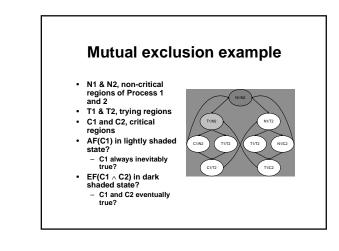
- One way to represent a finite state machine is as a state transition graph
 - S is a finite set of states
 - R is a binary relation that defines the possible transitions between states in S
 P is a function that assigns atomic
 - P is a function that assigns atomic propositions to each state in S
 e.g., that a specific process holds a lock
- Other representations include regular expressions, etc.





• Temporal formulae • Temporal logics allow us to say things like - Does some property hold true globally?





How does model checking work? (in brief!)

- An iterative algorithm that labels states in the transition graph with formulae known to be true
- For a query Q

Top figure

-Does some property inevitably hold true?

Bottom figure

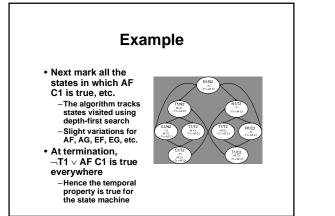
-Does some property potentially hold true?

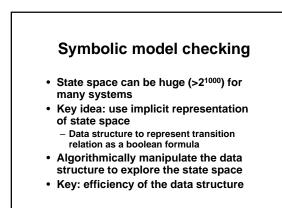
- the first iteration marks all subformulae of Q of length 1
- the second iteration marks them of length 2
- this terminates since the formula is finite
- The details of the logic indeed matter – But not at this level of description

Example

- Q = T1 ⇒ AF C1

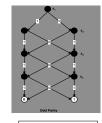
 If Process 1 is trying to acquire the mutex, then it is inevitably true it will get it sometime
- $Q = \neg T1 \lor AF C1$
- Rewriting with DeMorgan's Laws
 First, label all the states where T1, ¬T1, and C1 are true
 - These are atomic properties





Binary decision diagrams (BDDs)

- "Folded decision tree"
- Fixed variable orderMany functions have small
- BDDs – Multiplication is a notable
- exception
- Can represent
 State machines (transition
- functions) *and* – Temporal queries
 - . .



Due to Randy Bryant

BDD-based model checking

- Iterative, fixed-point algorithms that are quite similar to those in explicit model checking
- Applying boolean functions to BDDs is efficient, which makes the underlying algorithms efficient
- When the BDDs remain small, that is - Variable ordering is a key issue

BDD-based successes in HW

- IEEE Futurebus+ cache coherence protocol
- Control protocol for Philips stereo components
- ISDN User Part Protocol
- ...

Software model checking

- Finite state software specifications

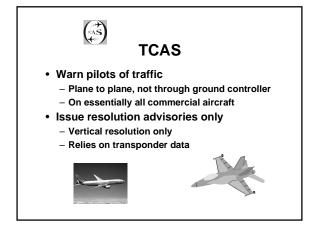
 Reactive systems (avionics, automotive, etc.)
- Hierarchical state machine specifications
 Not intended to help with proving
- Not intended to nelp with proving consistency of specification and implementation
 - Rather, checking properties of the specification itself

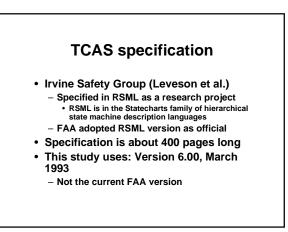
Why might it fail?

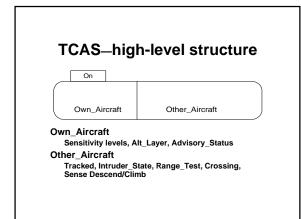
- Software is often specified with infinite state descriptions
- Software specifications may be structured differently from hardware specifications
 - Hierarchy
 - Representations and algorithms for model checking may not scale

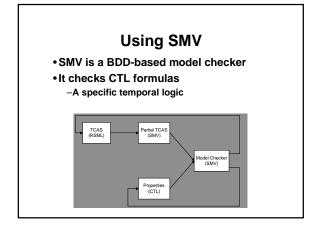
Our approach at UW-try it!

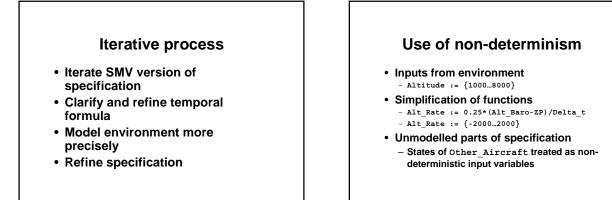
- Applied model checking to the specification of TCAS II
 - Traffic Alert and Collision Avoidance System
 In use on U.S. commercial aircraft
 - http://www.faa.gov/and/and600/and620/newtcas.htm
 - FAA adopted specification
- Initial design and development by Leveson et al.
 Later applied it to a statecharts description of an electrical power distribution system model of the B777
 - I can provide examples and papers

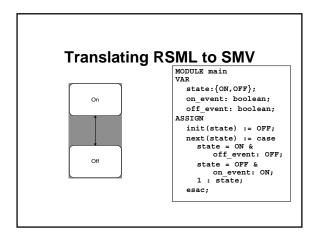


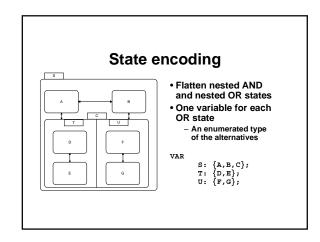












Events

- External—interactions with environment
- Internal-micro steps
- Synchrony hypothesis
- External event arrives
 - Triggers cascade of internal events (micro steps)
 - Stability reached before next external event
- Technical issues with micro steps

Non-deterministic transitions

- A machine is deterministic if at most one of T_A_B, T_A_C, etc. can be true
 - T_A_B represents the conditions under which a transition is taken from state A to state B
 - Else non-deterministic

Checking properties

- Initial attempts to check any property generated BDDs of over 200MB
- First successful check took 13 hours – Was reduced to a few minutes
- Partitioned BDDs
- · Reordered variables
- Implemented better search for counterexamples

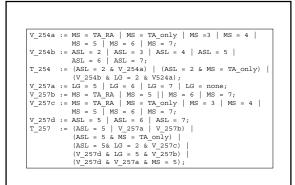
Property checking

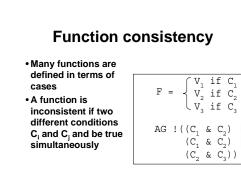
- Domain independent properties
 Deterministic state transitions
 - Function consistency
- Domain dependent
 Output agreement
 - Safety properties
- We used SMV to investigate some of these properties on TCAS' Own_Aircraft module

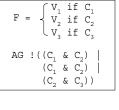
Disclaimer

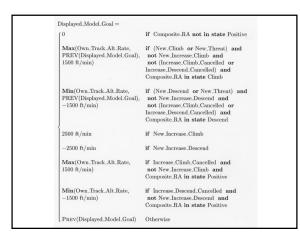
The intent of this work was to evaluate symbolic model checking of state-based specifications, not to evaluate the TCAS II specification. Our study used a preliminary version of the specification, version 6.00, dated March, 1993. We did not have access to later versions, so we do not know if the issues identified here are present in later versions.

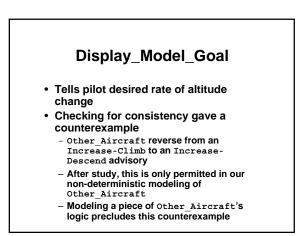
Deterministic transitions · Do the same conditions allow for nondeterministic transitions? Inconsistencies were found earlier by other methods [Heimdahl and Leveson] Identical conditions allowed transitions from Sensitivity Level 4 to SL 2 or to SL 5 · Our formulae checked for all possible non-determinism; we found this case, too Note: Earlier version of TCAS spec











Output agreement

- Related outputs should be consistent

 Resolution advisory
 - Increase-Climb, Climb, Descend, Increase-Descend
 - -Display Model Goal
 - Desired rate of altitude change
 - Between -3000 ft/min and 3000 ft/min
 - Presumably, on a climb advisory,
 Display_Model_Goal should be positive

Output agreement check

- AG (RA = Climb -> DMG > 0)
 If Resolution Advisory is Climb, then Display_Model_Goal is positive
- · Counterexample was found
 - $-t_0$: RA = Descend, DMG = -1500
 - t_1 : RA = Increase-Descend, DMG = -2500
- $-t_2$: RA = Climb, DMG = -1500

Limitations

- Can't model all of TCAS
 - Pushing limits of SMV (more than 200 bit variables is problematic)
 - Need some non-linear arithmetic to model parts of Other_Aircraft
 - New result that represents constraints as BDD variables and uses a constraint solver
- How to pick appropriate formulae to check?

Whence formulae?

"There have been two pilot reports received which indicated that TCAS had issued Descend RA's at approximately 500 feet AGL even though TCAS is designed to inhibit Descent RAs at 1,000 feet AGL. All available data from these encounters are being reviewed to determine the reason for these RAs." –TCAS web

Whence formulae?

 Jaffe, Leveson et al. developed criteria that specifications of embedded real-time systems should satisfy, including:

- All information from sensors should be used
- All information from sensors should be used
 Behavior before startup, after shutdown and during
- off-line processing should be specified
- Every state must have a transition defined for every possible input (including timeouts)
 Predicates on the transitions must yield deterministic behavior
- · Essentially a check-list, but a very useful one

What about infinite state?

- Model checking does not apply to infinite state specifications
- The iterative algorithm will not reach a fixpointTheorem proving applies well to infinite state
- specifications, but has generally proved to be unsatisfactory in practice
- One approach is to abstract infinite state specifications into finite state ones

 Doing this while preserving properties is hard
- D. Jackson et al.'s Nitpick approach
- Find counterexamples (errors), but don't "prove" anything



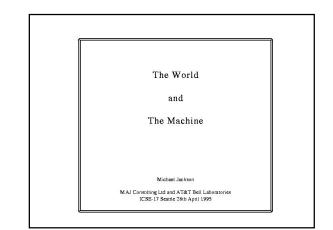
- The goal of model checking is to allow finite state descriptions to be analyzed and shown
 - to have particular desirable properties – Won't help when you don't want or need finite state
 - descriptions – Definitely added value when you do, but it's not
 - turnkey yet
 There's still a real art in managing model checking
 - Definitely feasible on modest sized systems

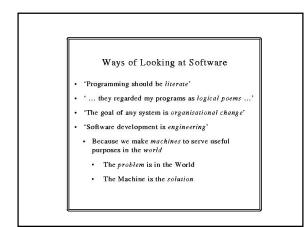
I know this was quick

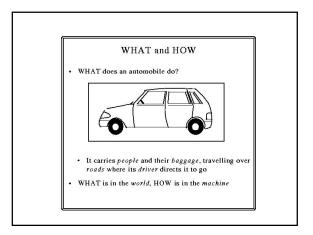
- My goal isn't to make you into model checking experts
 - But it might titillate one or two of you to learn more
- But rather to understand the sketches of what model checking is and why it is so promising for checking some classes of specifications

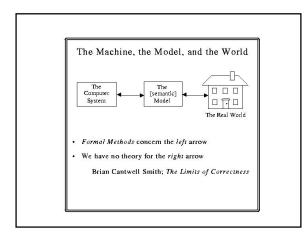
It's show time!

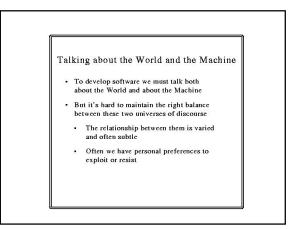
- Michael Jackson's keynote address at the 17th International Conference on Software Engineering (ICSE 17)
 - 1000 researchers, educators, and practitioners
 - Other keynoters: Fred Brooks, Michael Cusamano
- · Discussion on the mailing list...

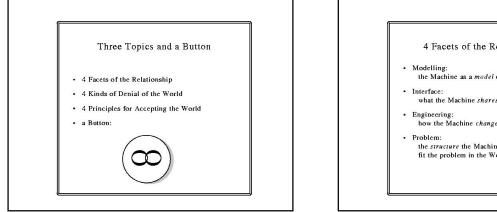


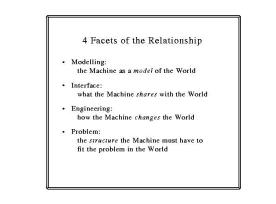


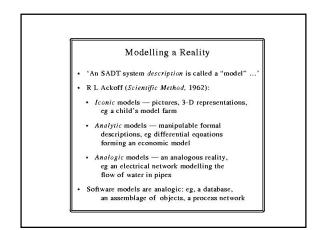


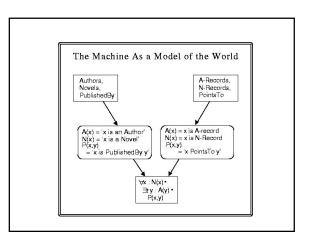


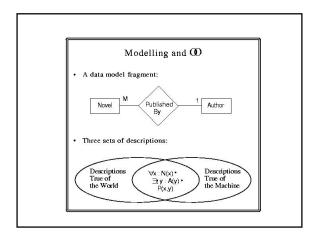


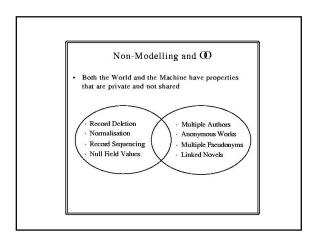


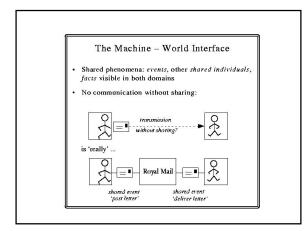


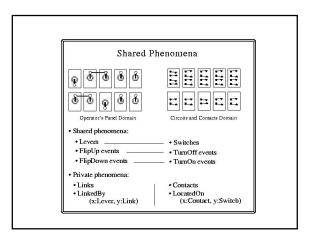


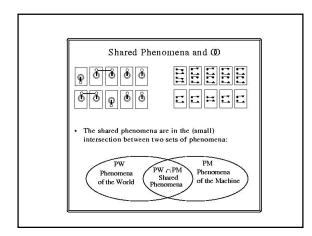


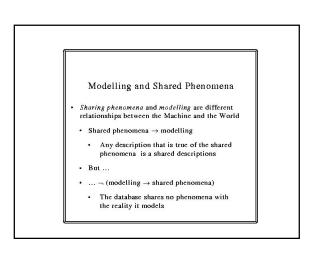


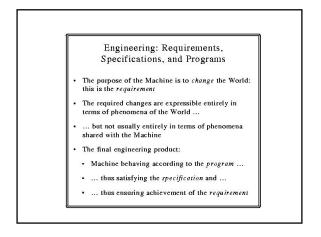


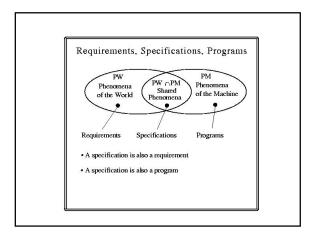


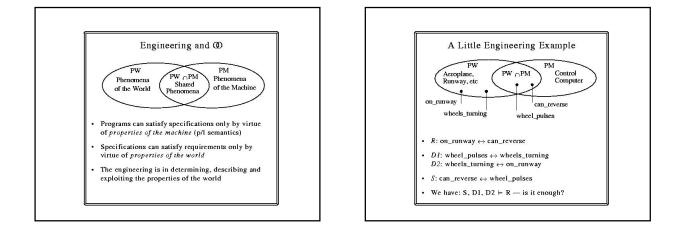


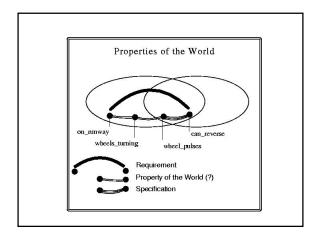


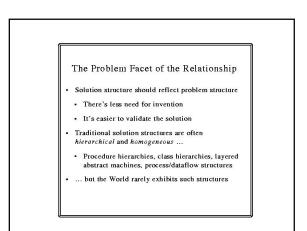


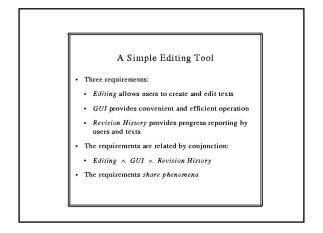


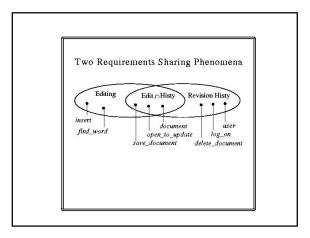


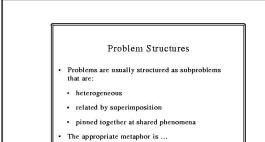












- · ... not assemblies and sub-assemblies
- ... but CYMK separations in colour printing

The World and Us (1)

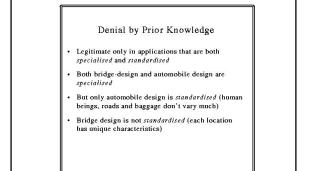
"The world is too much with us ..." – William Wordsworth

4 Kinds of Denial · How we may deny our involvement Denial by Prior Knowledge • Denial by Hacking Denial by Abstraction · Denial by Vagueness

Denial by Prior Knowledge

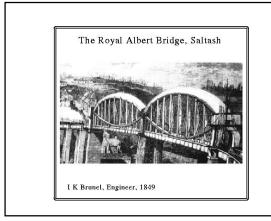
"We don't need a requirements capture phase. The problem is already well-defined; our task is merely to solve it."

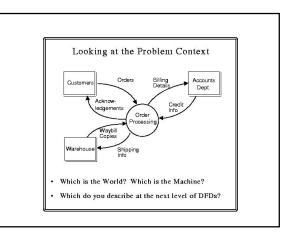
- Automobile designers don't have a requirements capture phase ...
- · The car shall be able to travel over snowdrifts and under water
- The car shall be able to lift a load of 5 tons
- The car shall accommodate 10 passengers each
- of weight up to 500 pounds
- ... it would be called 'Rethinking the Motor-car'

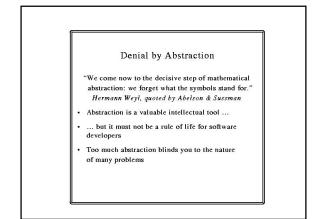


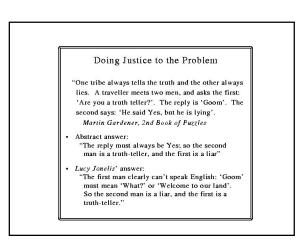
Denial by Hacking

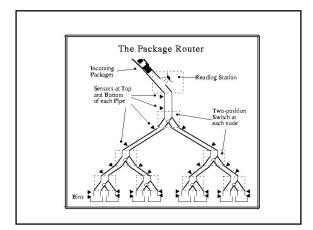
- Computers are beautiful and fascinating
- "... Miss Byron, young as she was, understood its working and saw the great beauty of the invention." Mrs De Morgan, on Ada's visit to Babbage, 1828
- Applications are often much less interesting
- "I came into this job to work with computers, not to be an amateur stockbroker." Member of failed development team, 1993
- The Machine is the developers' own creation; the World is not

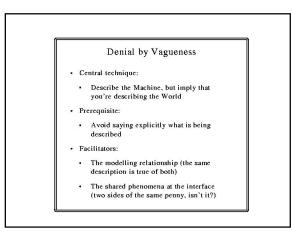


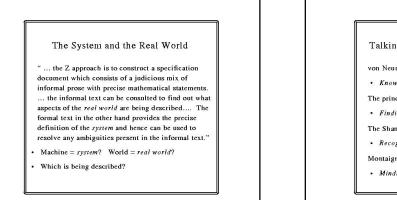


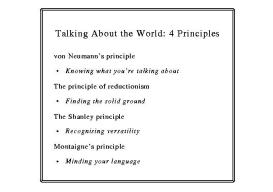


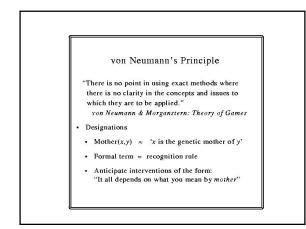


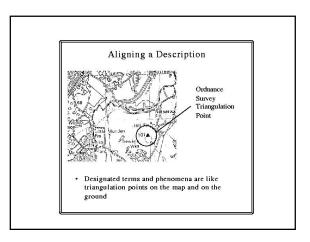


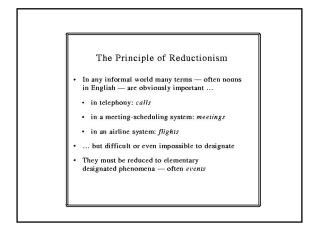


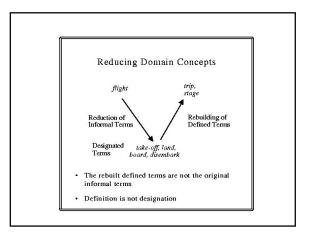


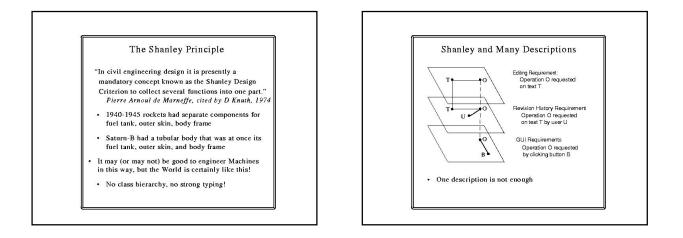


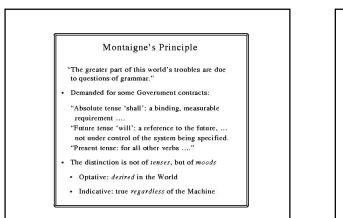


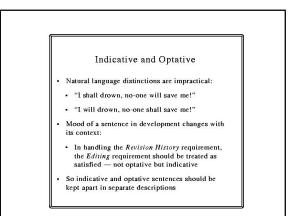


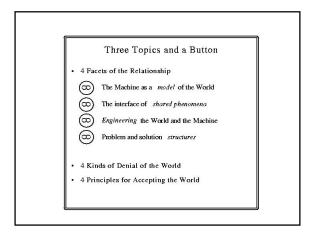


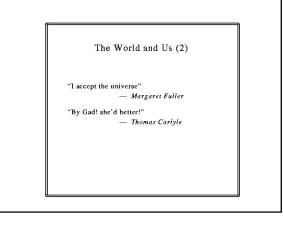












Good night

- Hope you enjoyed your night at the movies with Michael Jackson
- Let's leave discussion to the mailing list