

Luis Ceze

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Department of Computer Science

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- RESEARCH INTERESTS Parallel computer architecture. Also interested in compilation and programming models for parallelism.
- EDUCATION ◇ **University of Illinois at Urbana Champaign**, Urbana, IL.
Ph.D Computer Science, August 2007 (expected).
Advisor: Josep Torrellas
- ◇ **University of São Paulo, Polytechnic School**, Brazil.
B.S. and M.Eng. in Electrical Engineering, August 2002.
- SELECTED AWARDS AND HONORS ◇ **2007** Ross J. Martin Award, UIUC
Given by the College of Engineering to recognize outstanding research achievement.
- ◇ **2006** CW Gear Outstanding Graduate Award.
Given by the Computer Science Department to one outstanding graduate student each year.
- ◇ **2005** Paper selected for the IEEE Micro Top Picks Jan/Feb 2006
- ◇ **2005** WJ Poppelbaum Memorial Award.
For academic merit and creativity in computer architecture.
- ◇ **2005** IBM PhD Fellowship for 2004-2006
- ◇ **2004** IBM Outstanding Internship Award
- ◇ **2002** IBM Bravo! award for the contribution to the Blue Gene/L project
- SELECTED RESEARCH EXPERIENCE AND PUBLICATIONS ◇ **UIUC, I-ACOMA Group**. Research Assistant, August 2002 – present.
– Architecture for thread-level speculation (ICS'05a, ICS'05b, IEEE TopPicks'05).
– Compilation for thread-level speculation (Pac2'05, PPOPP'06).
– High memory-level parallelism architectures (CAL'04, WMPI'06, MICRO'06).
– Low-complexity thread-level speculation/transactional memory (ISCA'06).
– Data-centric synchronization in chip-multiprocessors (HPCA'07).
– Efficient enforcement of sequential consistency in chip-multiprocessors (ISCA'07).
- ◇ **IBM Research, Yorktown**. Grad Summer Intern, Summer 2006.
Mentor: Călin Cașcaval and Christoph von Praun. Manager: Martin Ohmacht.
– Evaluation, tools and programming model for simplifying parallel programming through speculation. (PPOPP'07, 3 invention disclosures)
- ◇ **IBM Research, Yorktown**. Grad Summer Intern, Summer 2004.
Mentor: Călin Cașcaval. Manager: Jong D. Choi.
– Assist threads for PERCS. SIMD units for redundant computation. (2 invention disclosures)
- ◇ **IBM Research, Yorktown**. Grad Summer Intern, Summer 2003.
Mentors: Monty Denneau and Jose Castaños. Manager: Christos Georgiou and Jose Moreira.
– Large number factorization benchmark for Cyclops64. Blue Gene/L control system.
- ◇ **IBM Research, Yorktown**. Co-op Pre-Professional Engineer, Jun 2001 – July 2002.
Mentor: Jose Moreira. Manager: Jose Moreira.
– Memory bandwidth study for Cyclops (HPCA'02).
– Blue Gene/L full system simulator (CWCE'03).
- ◇ **University of São Paulo**. Research Assistant. Networking Laboratory, Aug 1999 – Jun 2001.
– Multimedia synchronization framework for web learning (FIE'01).
– Digital signatures.
– Cisco Networking Academy instructor.
- SERVICE – Maintainer of SESC (sesc.sourceforge.net)
– Reviewer for MICRO, ISCA, HPCA, ICS, P=ac2, PPOPP, PLDI, LCPC, CAL, TACO and Journal of Parallel Computing.
– Member of ACM and IEEE.
– Organizer of computer architecture reading groups at UIUC and IBM.