Xbus is a streaming nibble based synchronous bus interface between the Xilinx and the uPC (uPC is the master, Xilinx is the slave). The uPC interface consists of two C procedures at the moment:

XWrite(addr, data);
data = XRead(addr);

The interface at the Xilinx side is a synchronous read/write memory port interface (output: read, write, address, data  input: busy, data).