

Tell us what the following numbers mean if interpreted as (i) 2's comp (ii) Unsigned.

a. 0x20080024

b. 0xAC4F0004

20080024 is positive in 2's comp, so same as unsigned:

$$2 \times 16^7 + 8 \times 16^4 + 2 \times 16^1 + 4 = 537395236_2$$

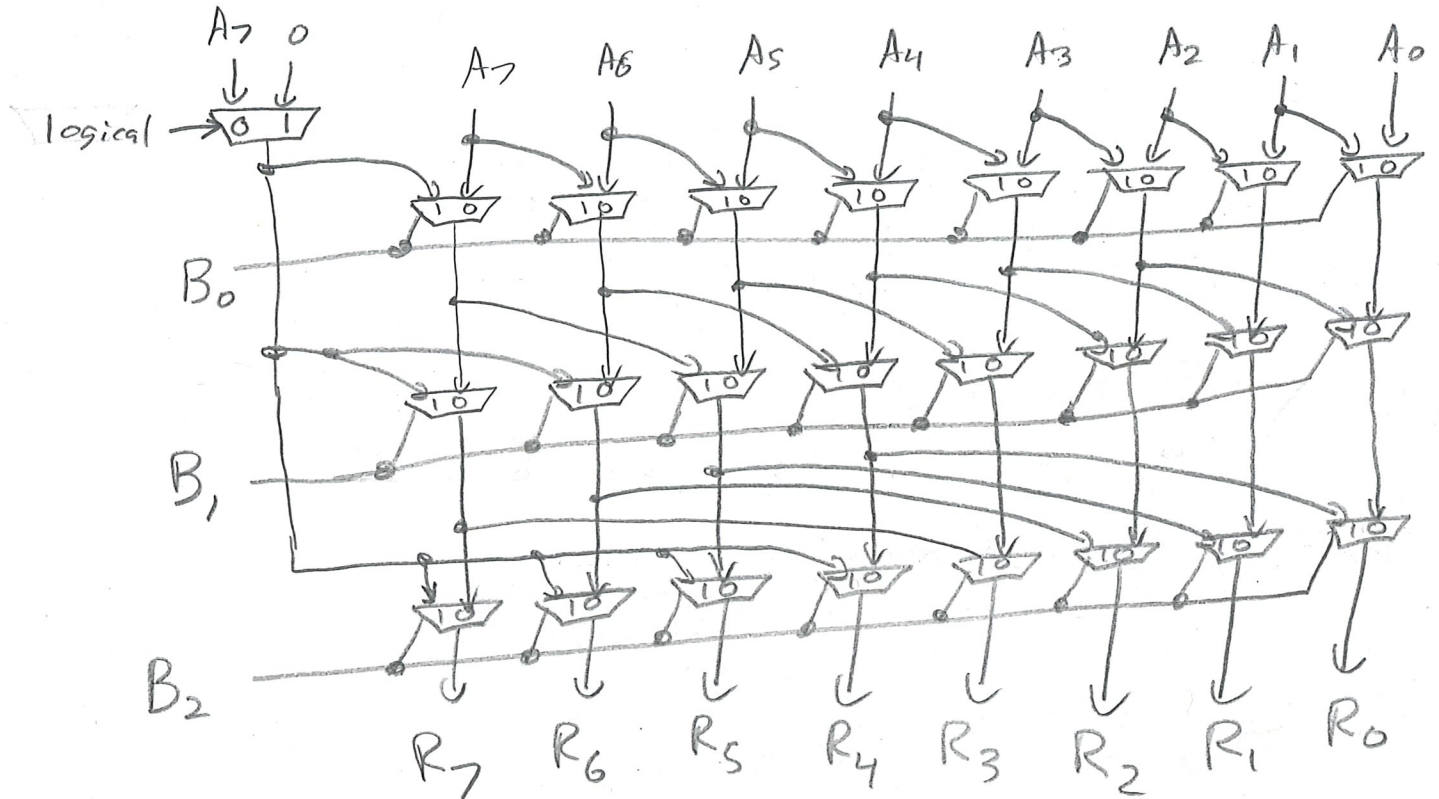
AC4F0004 unsigned

$$10 \times 16^7 + 12 \times 16^6 + 4 \times 16^5 + 15 \times 16^4 + 4 = 2890858500$$

2's comp

$$\begin{aligned}
 &= \text{---} \quad \text{A} \quad \text{C} \quad \text{4} \quad \text{F} \quad \text{0} \quad \text{0} \quad \text{0} \quad \text{4} \\
 &= \text{---} (1010 \ 1100 \ 0100 \ 1111 \ 0000 \ 0000 \ 0000 \ 0100) \\
 &= \text{---} (0101 \ 0011 \ 1011 \ 0000 \ 1111 \ 1111 \ 1111 \ 1011 + 1) \\
 &= \text{---} \quad \text{5} \quad \text{3} \quad \text{B} \quad \text{0} \quad \text{F} \quad \text{F} \quad \text{F} \quad \text{C} \\
 &= \text{---} \quad \text{1404108796}
 \end{aligned}$$

In class we build a left shifter. Using the same logic, create a right shifter for 8-bit values. It takes in an 8-bit value $A_7..A_0$ to be shifted, and a 3-bit shift amount $B_2..B_0$. It also gets a signal "Logical", which when true performs a logical right shift, while when false performs an arithmetic shift (same as division by 2^B on 2's comp numbers). Your design should require only 2:1 Muxes, and should be as simple as possible.



Assuming the values given below were unsigned 6-bit values, compute the following results.
Your answers should be a 12-bit unsigned value

A: 100101

B: 001011

C: 110010

i.) A * B

ii.) A * C

i.)

$$\begin{array}{r}
 100101 \\
 \times 001011 \\
 \hline
 100101 \quad \times 1 \\
 000101 \quad \times 2 \\
 000101 \quad \times 0 \\
 000000 \quad \times 8 \\
 100101 \quad \times 0 \\
 000000 \quad \times 0 \\
 \hline
 0011001011
 \end{array}$$

ii.)

$$\begin{array}{r}
 100101 \\
 \times 110010 \\
 \hline
 000000 \quad \times 0 \\
 100101 \quad \times 2 \\
 000000 \quad \times 0 \\
 000000 \quad \times 0 \\
 000000 \quad \times 0 \\
 100101 \quad \times 16 \\
 000000 \quad \times 32 \\
 \hline
 11100111010
 \end{array}$$

Suppose you wish to run a program P with 7.5×10^9 instructions on a 5GHz machine with a CPI of 0.8.

a.) What is the expected CPU time?

b.) When you run P, it takes 3 seconds of wall clock time to complete. What is the percentage of the CPU time P received?

$$a.) \text{ Exec Time} = \text{instr} \times \text{CPI} \times \frac{1}{\text{Rate}}$$

$$7.5 \times 10^9 \times 0.8 \times \frac{1}{5 \times 10^9}$$

$$= 1.5 \times 0.8 = 1.2 \text{ seconds}$$

$$b.) \frac{1.2}{3} = 40\%$$

Consider program P, which runs on a 1 GHz machine M in 10 seconds. An optimization is made to P, replacing all instances of multiplying a value by 4 (mult X, X, 4) with two instructions that set x to x+x twice (add x, x, x; add x, x, x). Call this new optimized program P'. The CPI of a multiply instruction is 4, and the CPI of an add is 1. After recompiling, the program now runs in 9 seconds on machine M. How many multiplies were replaced by the new compiler?

$$\text{OLD: } \text{Exec} = \text{instr} \times \text{CPI} \times \frac{1}{\text{Rate}}$$

$$\text{OLD: } 10 = (\text{Norm-instr} \times \text{Norm-CPI} + \text{mult} \times 4) \times \frac{1}{1 \times 10^9}$$

$$10 \times 10^9 = (\text{Norm-instr} \times \text{Norm-CPI} + 4 \times \text{mult})$$

$$\text{NEW: } 9 = (\text{Norm-instr} \times \text{Norm-CPI} + (2 \times \text{mult}) \times 1) \times \frac{1}{1 \times 10^9}$$

$$9 \times 10^9 = (\text{Norm-instr} \times \text{Norm-CPI} + 2 \times \text{mult})$$

$$\text{OLD-NEW: } 10 \times 10^9 - 9 \times 10^9 = (\text{Norm-instr} \times \text{Norm-CPI} + 4 \times \text{mult}) - (\text{Norm-instr} \times \text{Norm-CPI} + 2 \times \text{mult})$$

$$1 \times 10^9 = 4 \times \text{mult} - 2 \times \text{mult}$$

$$1 \times 10^9 = 2 \times \text{mult}$$

$$\underline{\underline{0.5 \times 10^9 = \text{mult}}}$$

Your company could speed up a Java program on their new computer by adding hardware support for garbage collection. Garbage collection currently comprises 20% of the cycles of the program. You have two possible changes to the machine. The first one would be to automatically handle garbage collection in hardware. This causes an increase in cycle time by a factor of 1.2. The second would be to provide for new hardware instructions to be added to the ISA that could be used during garbage collection. This would halve the number of instructions needed for garbage collection but increase the cycle time by 1.1. Which of the two options, if either, should you choose?

$$\text{Baseline: ExecTime} = \text{Original}$$

$$\text{HW: ExecTime} = 80\% \times \text{Original} \times 1.2 = .96 \times \text{Original}$$

$$\begin{aligned} \text{CInstr: ExecTime} &= 1.1 \times (.8 \times \text{Original} + .2 \times \text{Original} / 2) \\ &= 1.1 \times (.8 + .1) \times \text{Original} \\ &= 1.1 \times 0.9 \times \text{Original} \\ &= .99 \times \text{Original} \end{aligned}$$

HW is best